Claims

1. Arrangement comprising a microprocessor, a demagnetization circuit, and a switched mode power supply having a normal mode and a low power mode, the microprocessor being coupled to the switched mode power supply, **wherein** signals from the microprocessor for controlling the low power mode and the demagnetization circuit are coupled via the same output to the demagnetization circuit and to the switched mode power supply.

2. Arrangement according to claim 1, **wherein** the microprocessor comprises a single pin for controlling the low power mode as well as the demagnetization circuit.

3. Arrangement according to claim 1, **wherein** the arrangement provides an on-indicative signal only present in the normal mode of the switched mode power supply, and that the control signal from the microprocessor is coupled to the demagnetization circuit in dependency of the power on-indicative signal.

4. Arrangement according to claim 1, **wherein** the control signal from the microprocessor and a power on-indicative signal are combined via a logical AND combination, for example via an AND gate, for controlling the demagnetization circuit.

5. Arrangement according to claim 3, **wherein** the power on-indicative signal is a supply voltage being provided by the switched mode power supply only during the normal mode.

6. Arrangement according to claim 1, **wherein** the control signal from the microprocessor is in the low power mode a square wave signal for providing a burst mode, the

duty cycle of the square wave signal defining the switching cycles of the switched mode power supply.

7. Arrangement according to claim 1, **wherein** the control signal from the microprocessor for controlling the demagnetization circuit is "enable" for a time sufficient to provide a demagnetization of a picture tube, when the switched mode power supply is switched to the normal mode.

8. Arrangement according to claim 6, **wherein** the "enable" signal for the demagnetization circuit has a duration of 0,5 to 3 sec., and is switched to "low" after the demagnetization phase.

9. Arrangement comprising a microprocessor, a demagnetization circuit, and a switched mode power supply having a normal mode and a low power mode, the microprocessor being coupled to the switched mode power supply, **wherein** the microprocessor comprises one single pin for controlling the low power mode as well as the demagnetization circuit.

10. Display unit, comprising an arrangement according to claim 9.